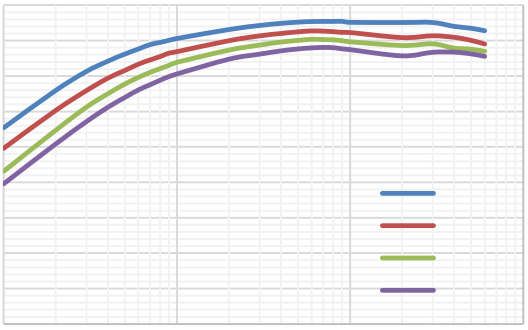
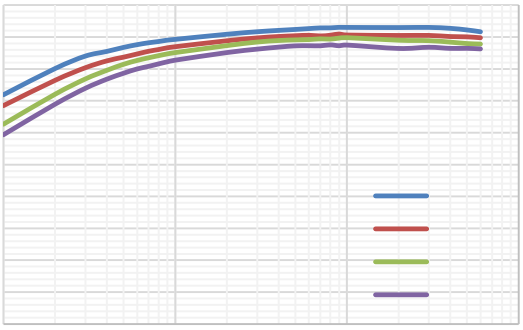
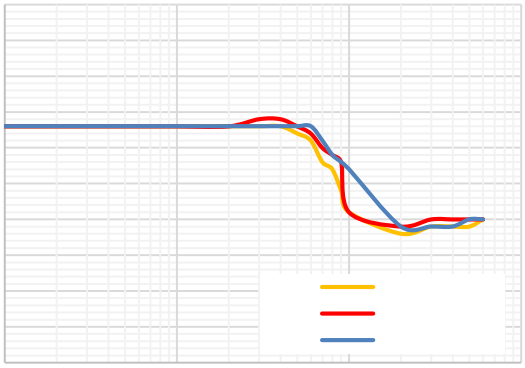
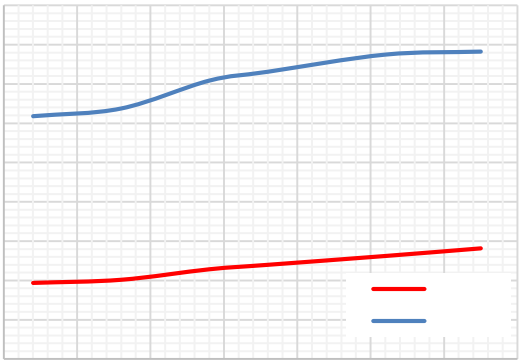


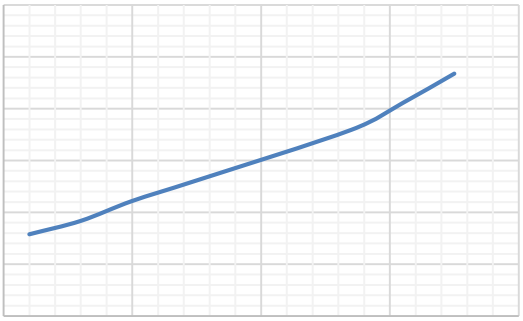




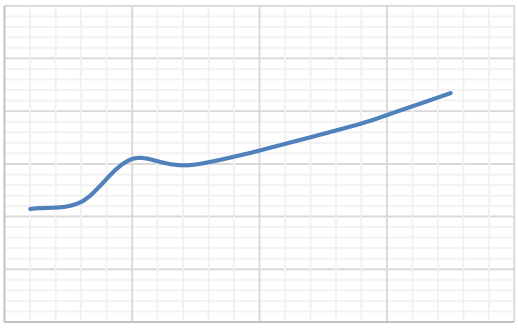
Output Current (A)



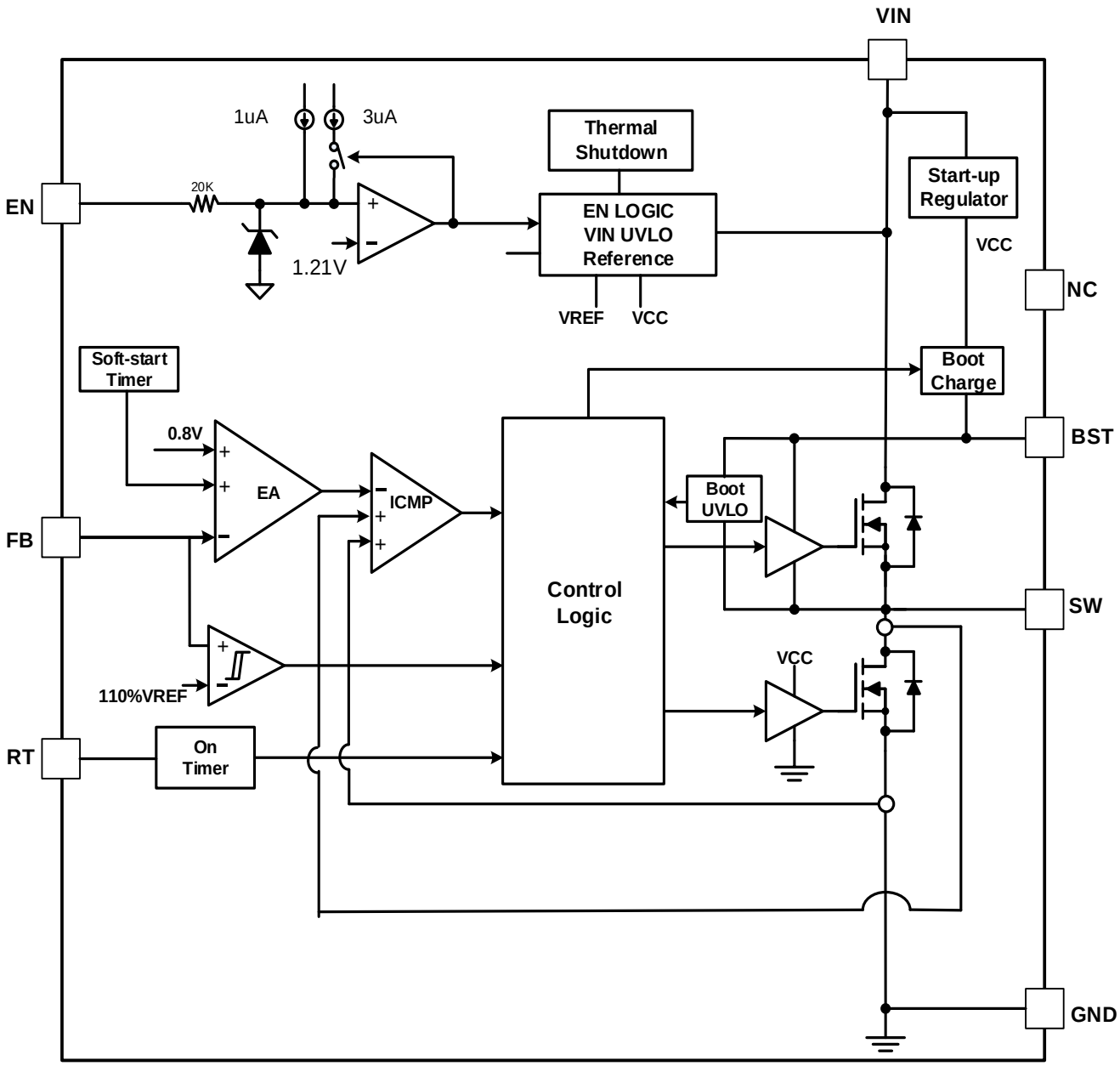
Input Votlage (V)

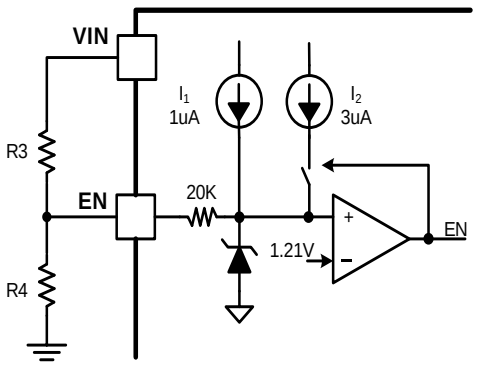


Temperature (C)



Temperature (C)


VIN
1uA
3uA
Thermal Shutdown
Start-up Regulator
EN
20K
EN LOGIC
VIN UVLO
Reference
VCC
1.21V
VREF
VCC
NC
Soft-start Timer
Boot Charge
0.8V
BST
EA
ICMP
Boot UVLO
FB
Control Logic
SW
VCC
110%VREF
RT
On Timer
GND



±1% tolerance

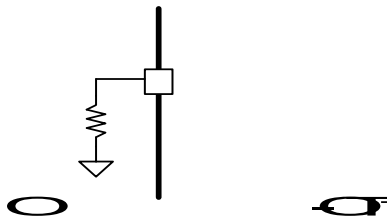
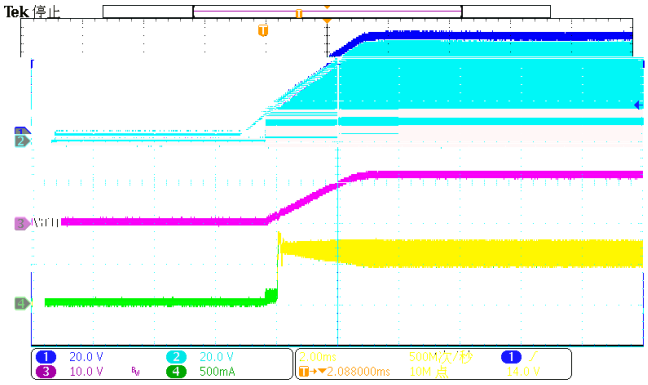


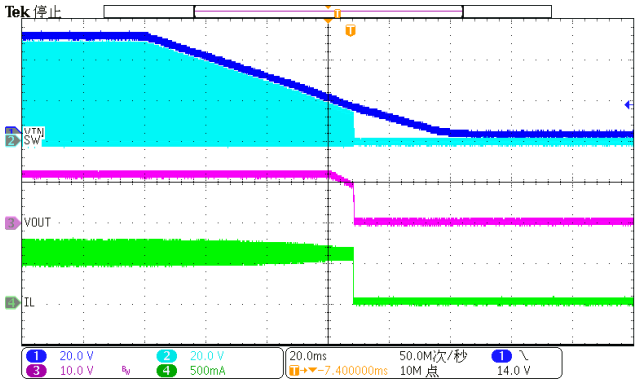
Table 1. R_5, R_6Value for Common Output Voltage (Room Temperature)

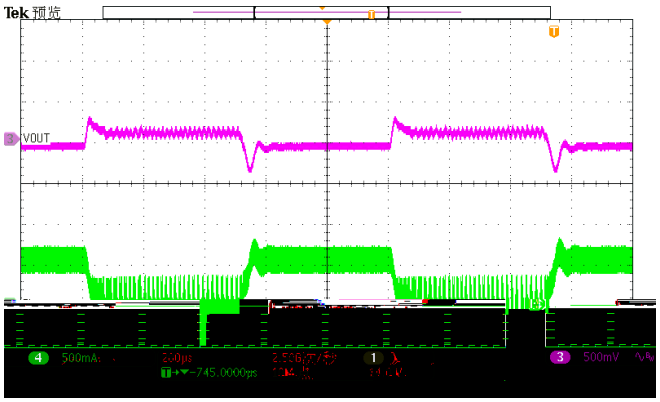
-
-

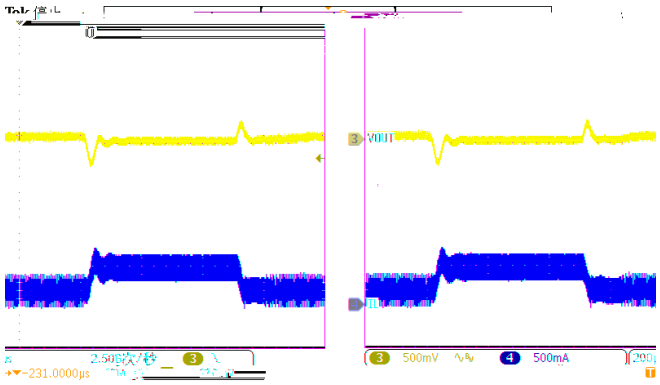
-
-
-
-
-
-

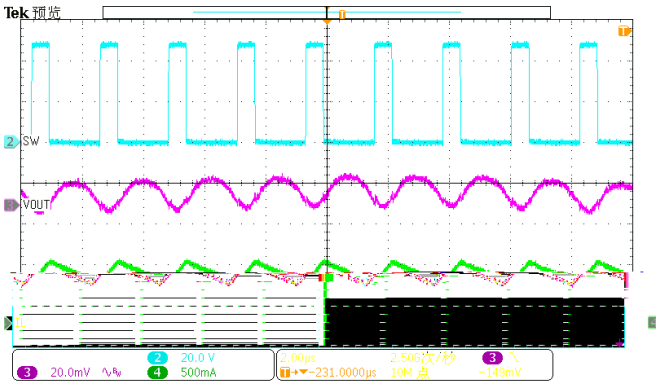
-
-
-
-

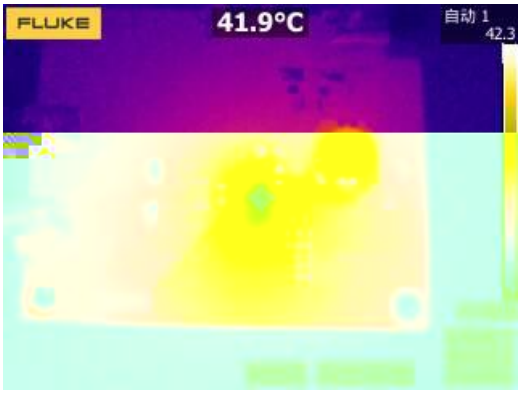

Tek 停止
VIN
SW
VOUT
IL
20.0 V
10.0 V
20.0 V
500mA
2.00ms
500M次/秒
10M 点
2.088000ms
14.0 V

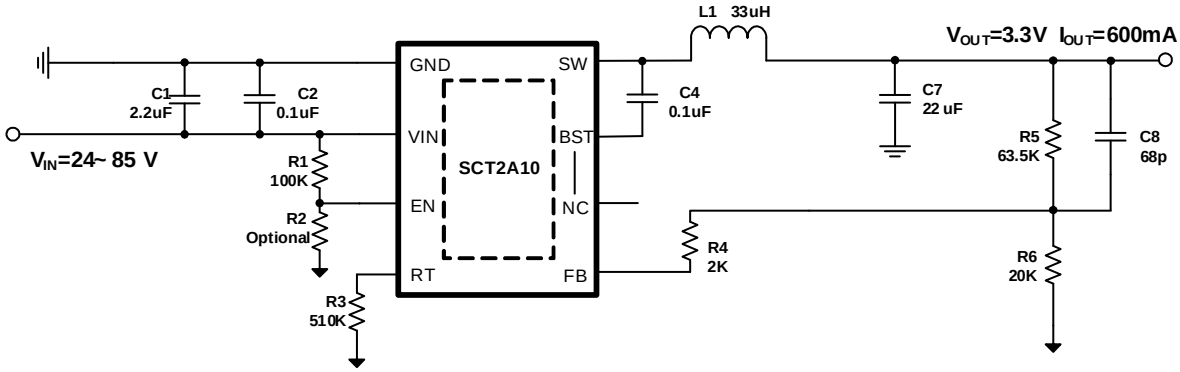

Tek 停止
VIN
SW
VOUT
IL
20.0 V
10.0 V
20.0 V
500mA
20.0ms
50.0M次/秒
10M 点
-7.400000ms
14.0 V

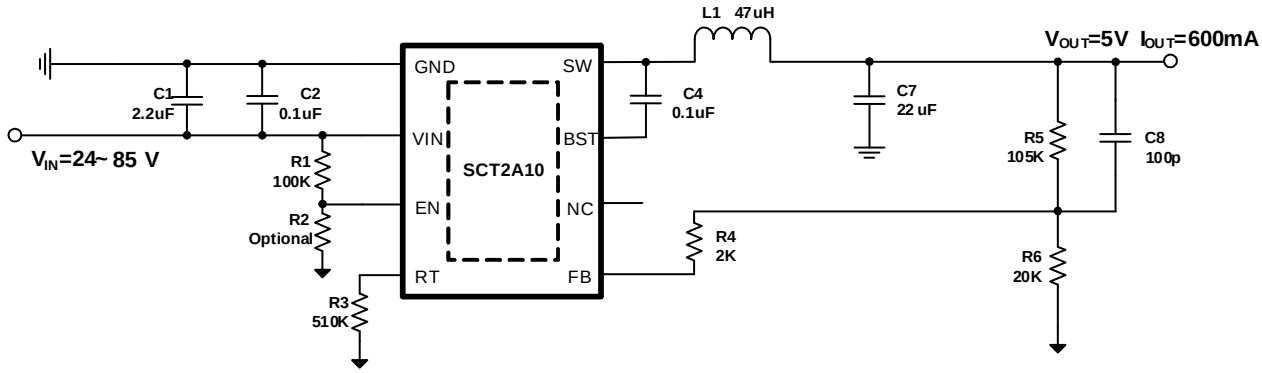

Tek 预览
VOUT
500mA
500mV
200µs
2.50G次/秒
10M 点
-745.0000µs

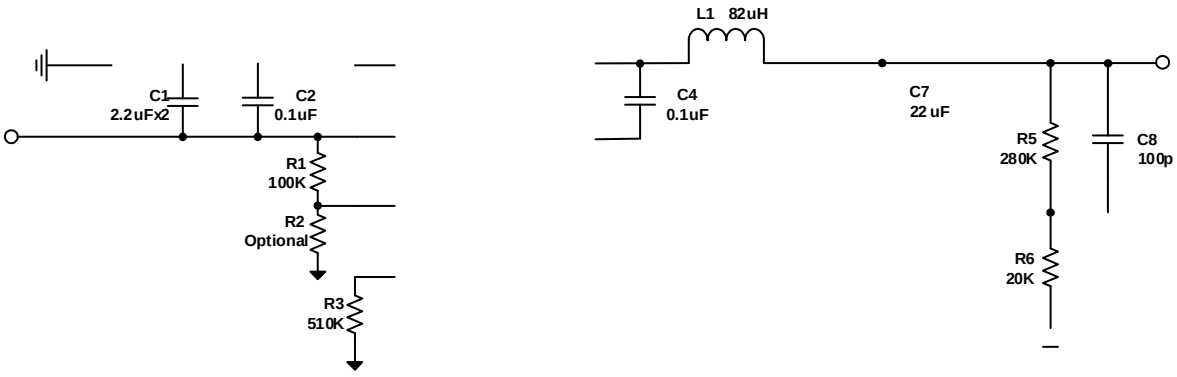

VOUT
2.50G次/秒
-231.0000µs
500mV
500mA


Tek 预览
SW
VOUT
IL
20.0 V
500mA
20.0mV
2.00µs
2.50G次/秒
10M 点
-231.0000µs
-149mV


FLUKE
41.9°C
自动 1
42.3


L1 33uH
V_{OUT}=3.3V I_{OUT}=600mA
GND
SW
VIN
BST
SCT2A10
EN
NC
RT
FB
C1
2.2uF
C2
0.1uF
C4
0.1uF
C7
22 uF
R5
63.5K
C8
68p
V_{IN}=24~ 85 V
R1
100K
R2
Optional
R4
2K
R6
20K
R3
510K


L1 47uH
V_{OUT}=5V I_{OUT}=600mA
GND
SW
VIN
BST
SCT2A10
EN
NC
RT
FB
C1
2.2uF
C2
0.1uF
C4
0.1uF
C7
22 uF
R5
105K
C8
100p
V_{IN}=24~ 85 V
R1
100K
R2
Optional
R4
2K
R6
20K
R3
510K


L1 82uH
C1
2.2uFx2
C2
0.1uF
C4
0.1uF
C7
22 uF
R5
280K
C8
100p
R1
100K
R2
Optional
R6
20K
R3
510K

1.

2.
3.

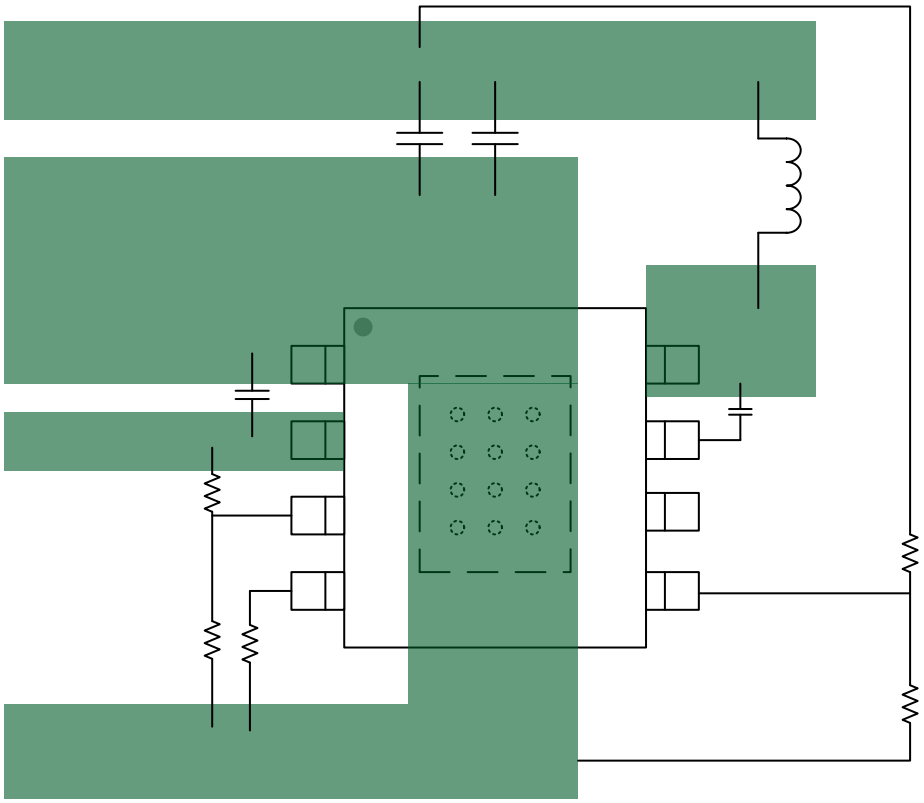
4.

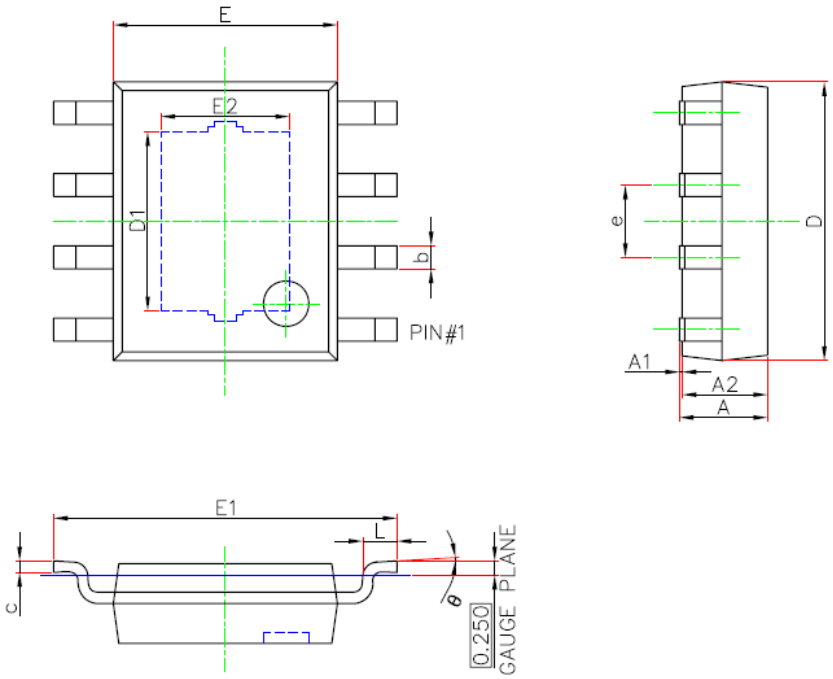
5.

6.

7.

8.
9.





θ				

1.
2.
3.
4.
5.
6.