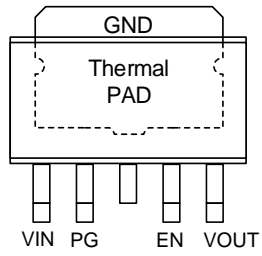
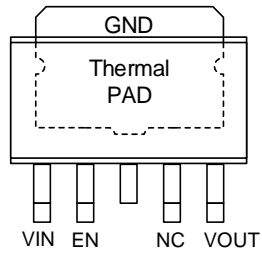
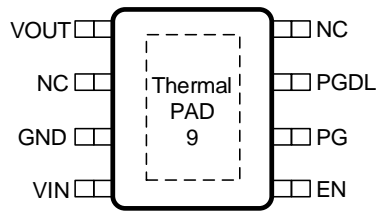
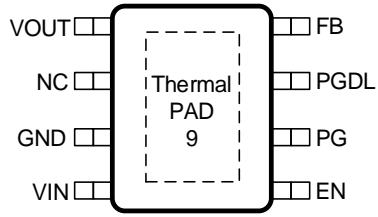

3V-40V Vin, 500mA, Ultra-Low Quiescent Current LDO with Power-Good

LV

Release to production().
Update the ELECTRICAL CHARACTERISTICS.

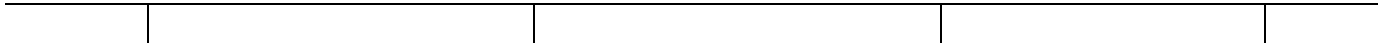
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			F50BQ		
			F33BQ		
			5F50Q		
			5F33Q		
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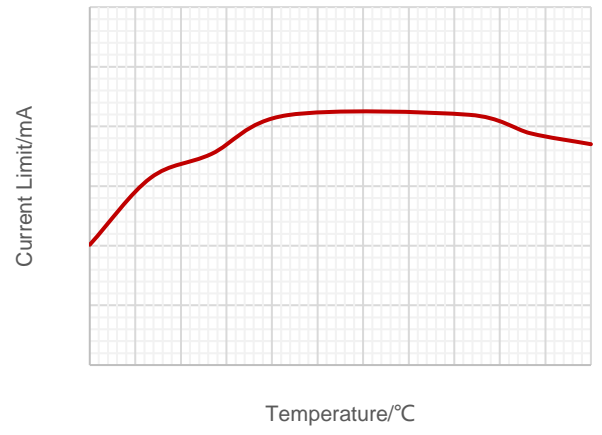
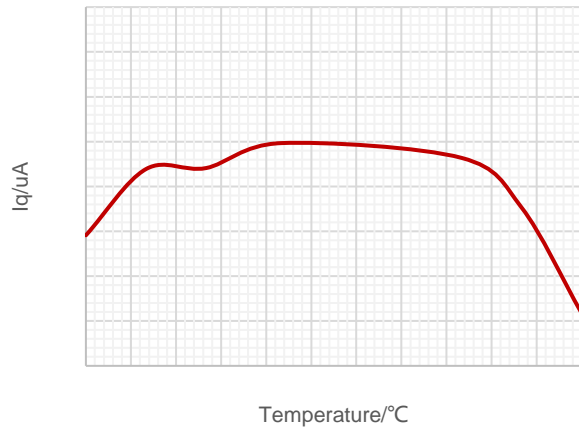
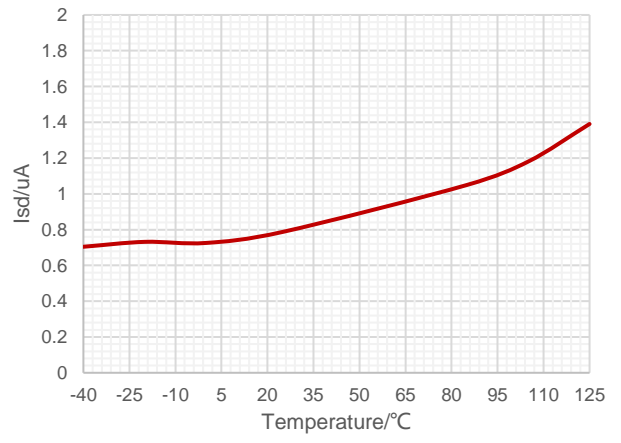
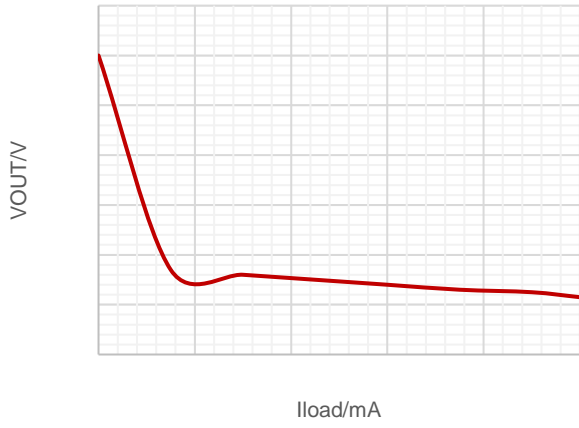


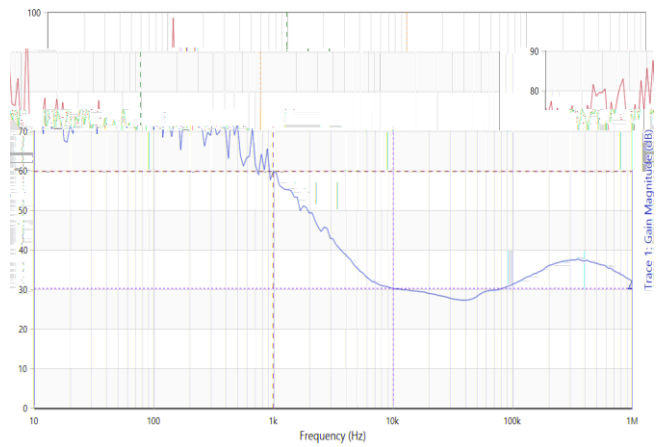
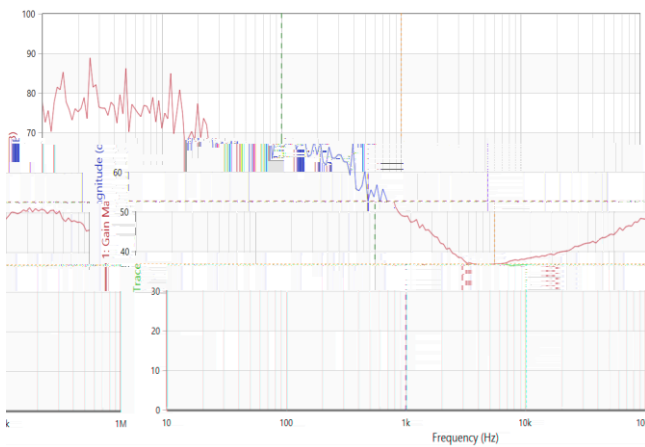
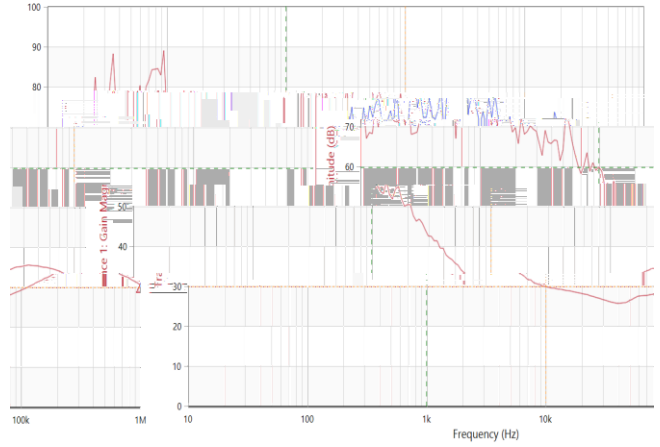
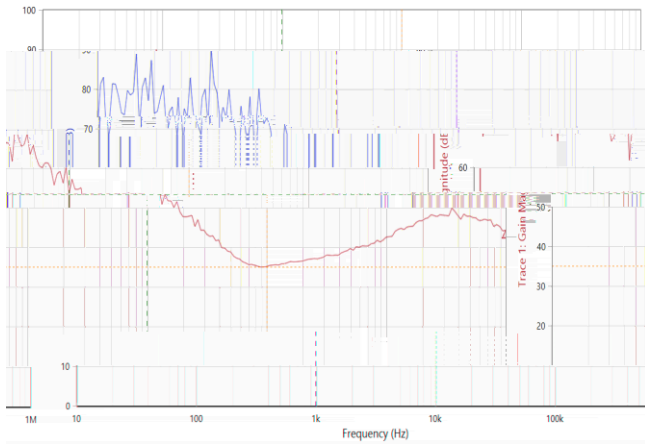
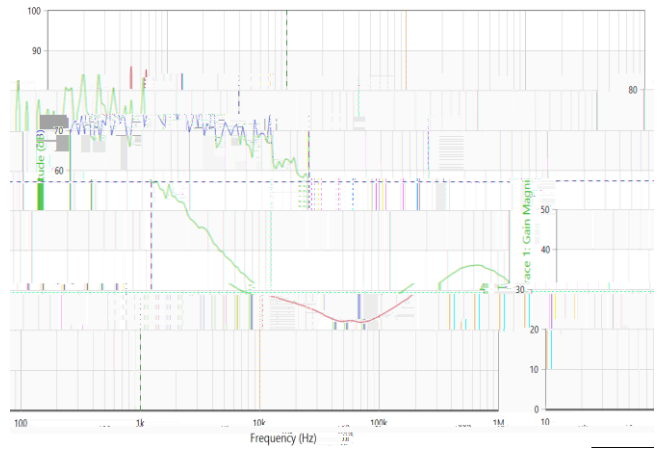
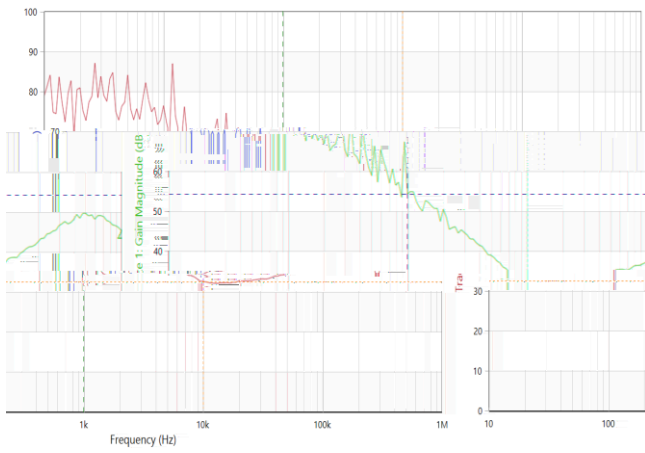
(1)
(2)

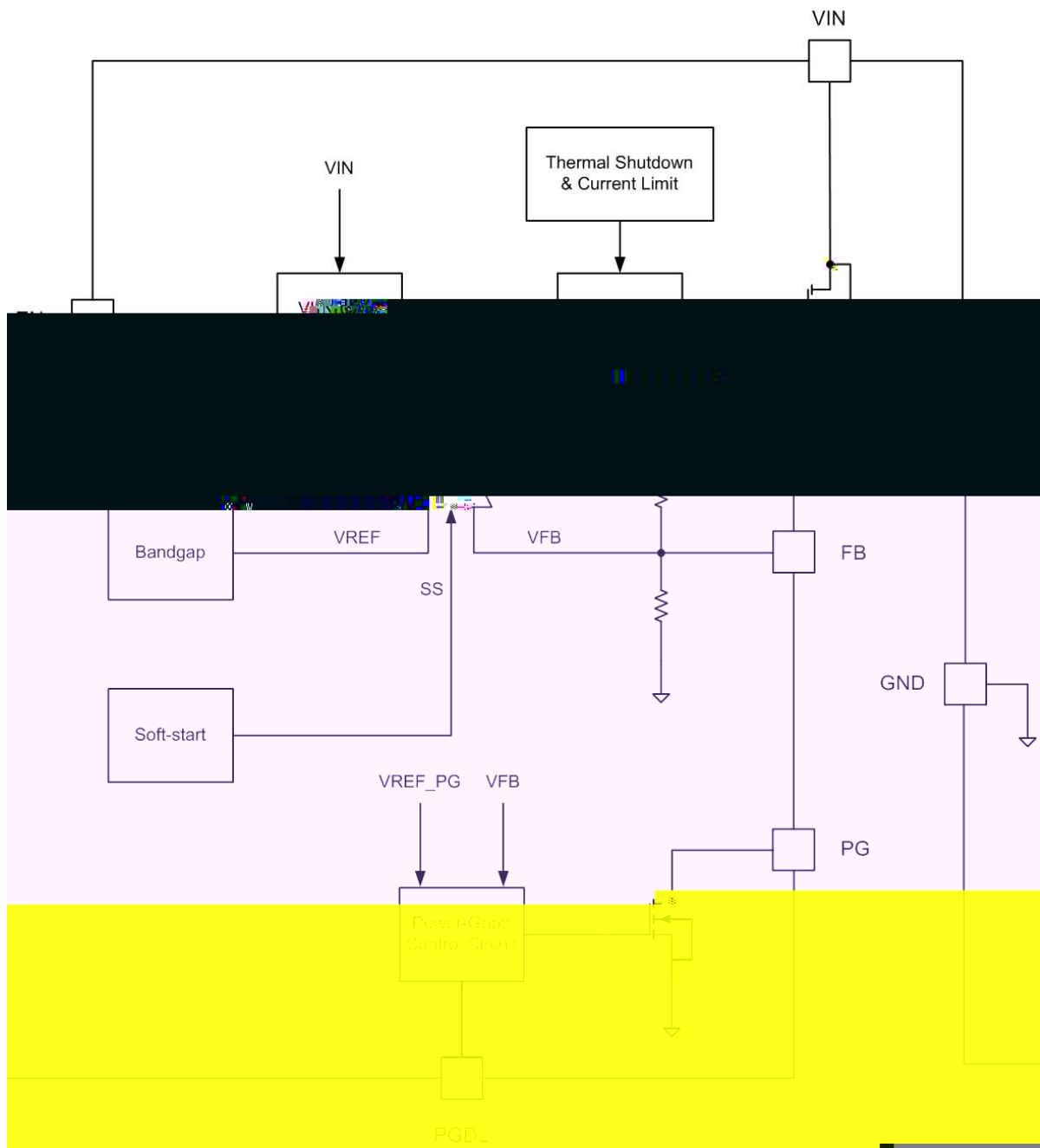
PARAMETER (4-layer)			UNIT
R _{JA,EVM}			
PARAMETER (4-layer)			UNIT
R _{JA,EVM}			

- (1)
- (2)
- (3)



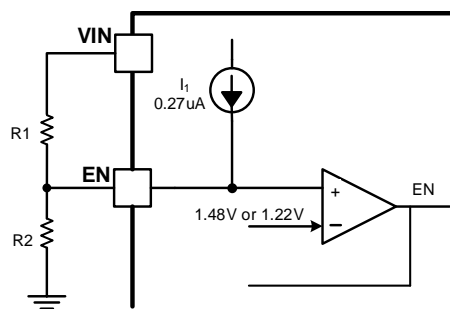




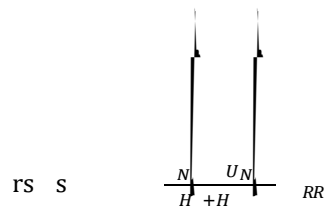
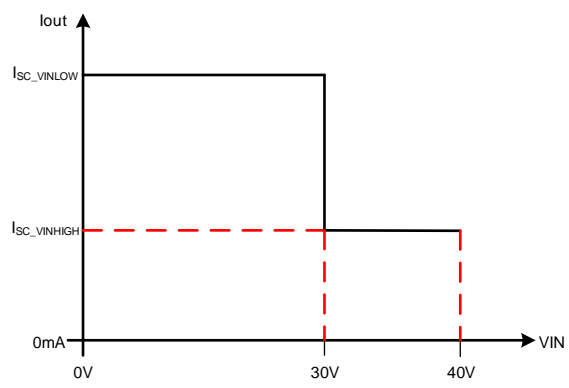


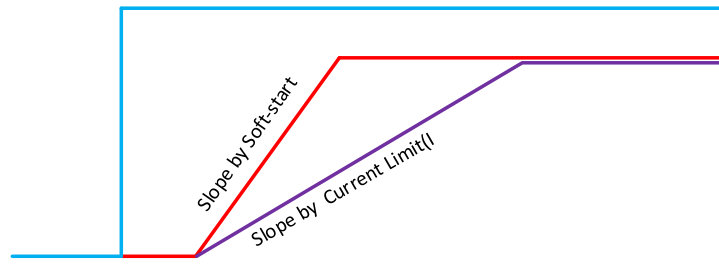
$$r_{DMG} = \frac{1}{1}$$

$$r_{DMG} = \frac{1}{1}$$



VIN_HIGH Control



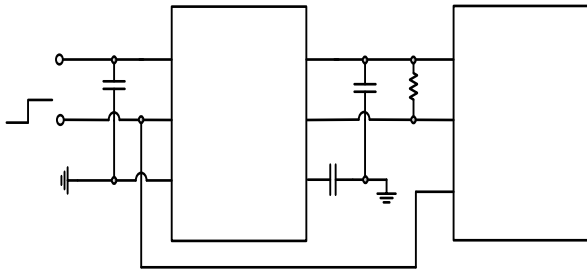


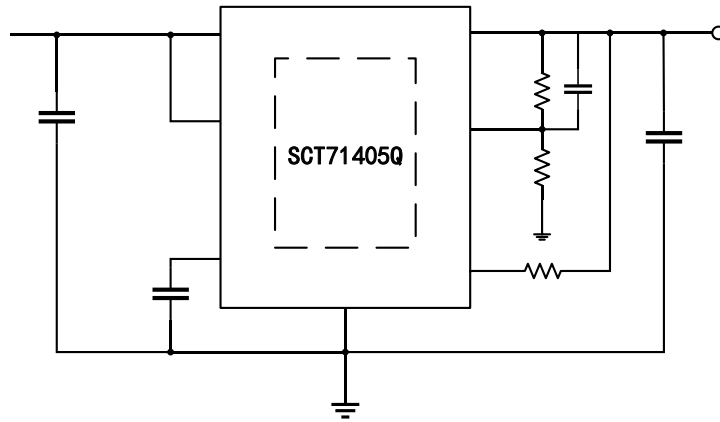
$$O C \quad \frac{S_{OFC} H_{OFC}}{U_{OFC}}$$

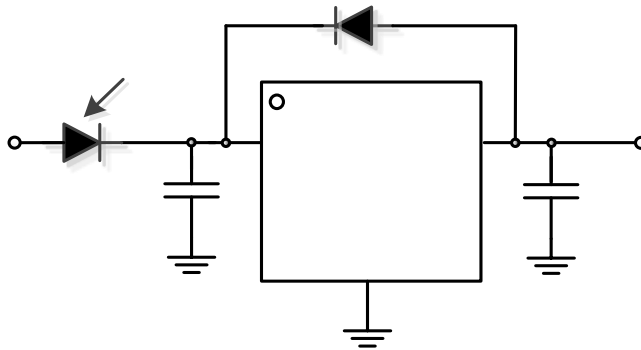
O C

O C

O C

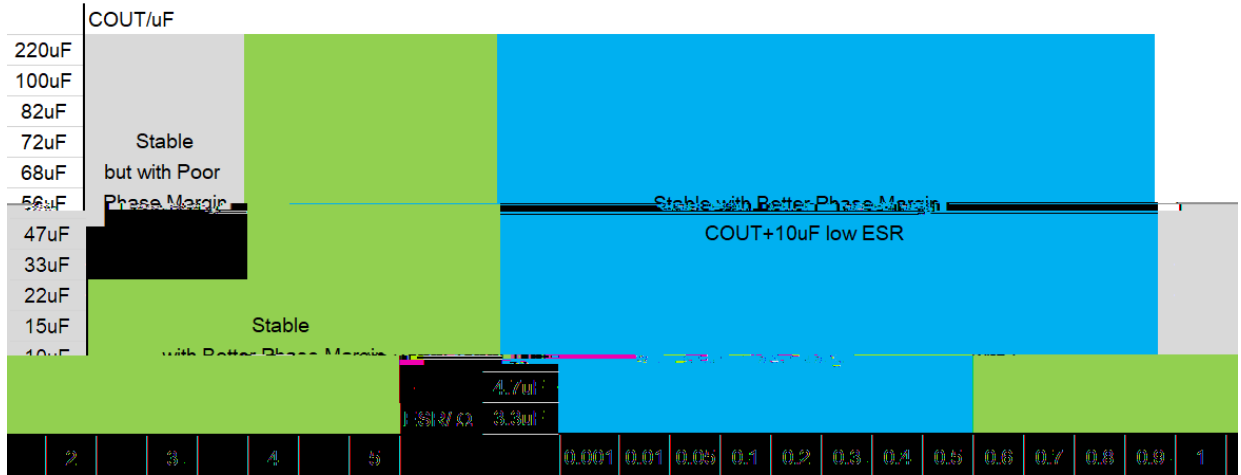






$$\frac{U_N}{U_{DE}}$$

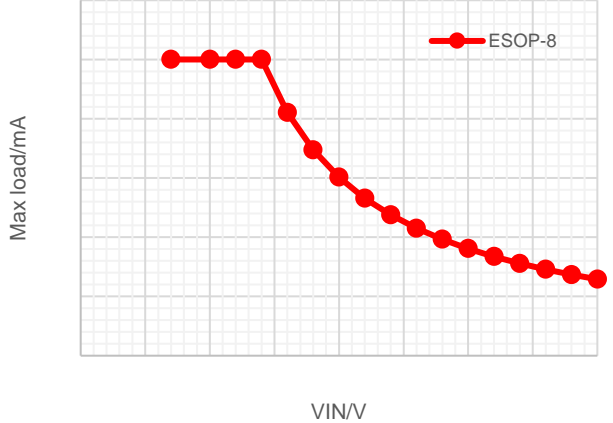
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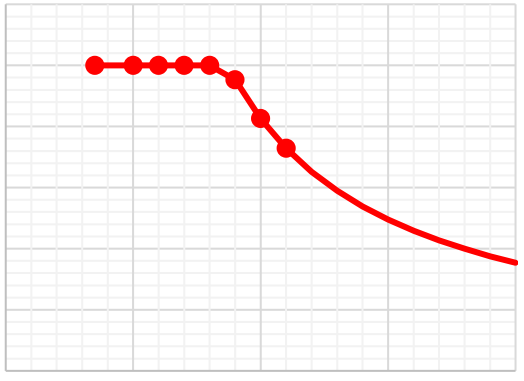
C HM NTS NTS HM MC

I C I DUL

		°C	°C
		°C	°C



VIN/V



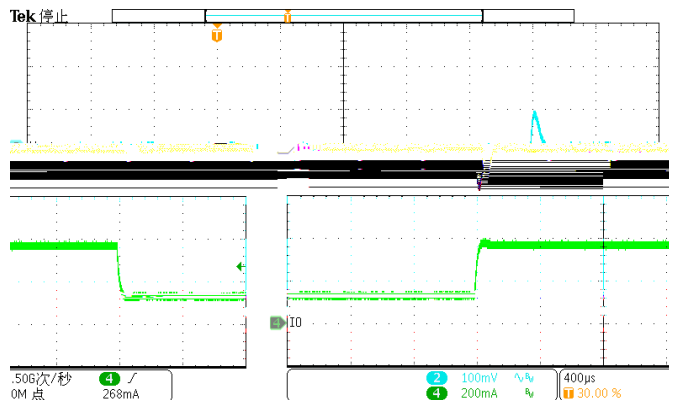
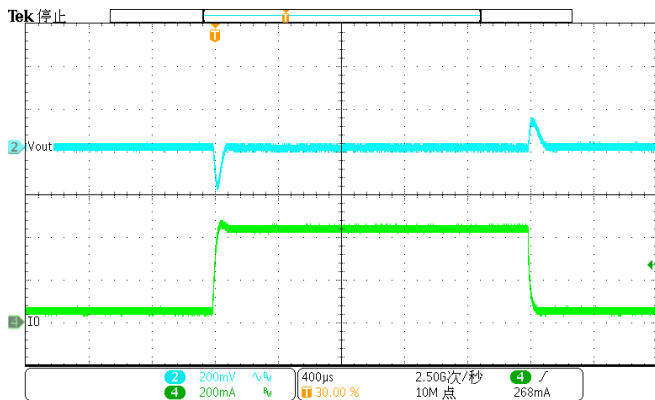
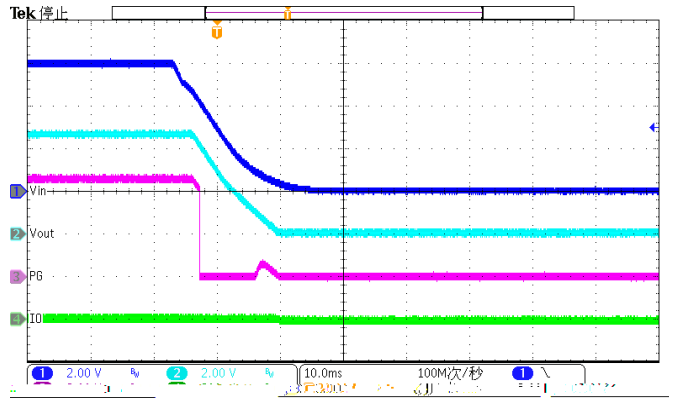
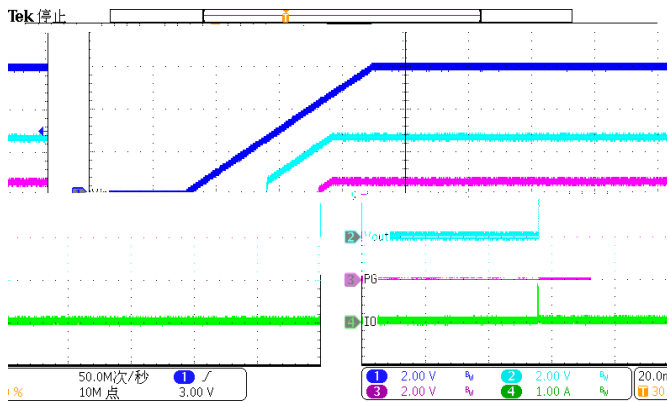
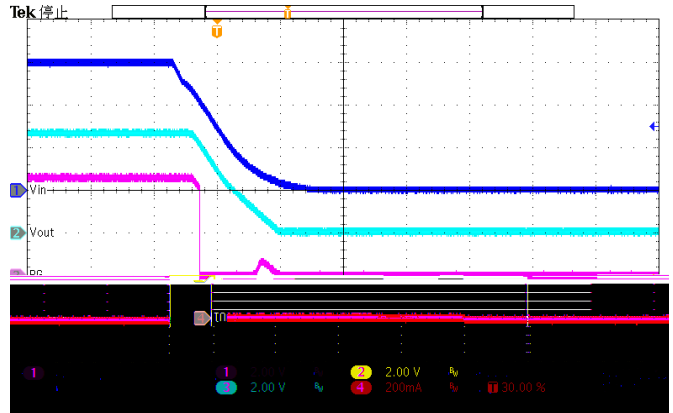
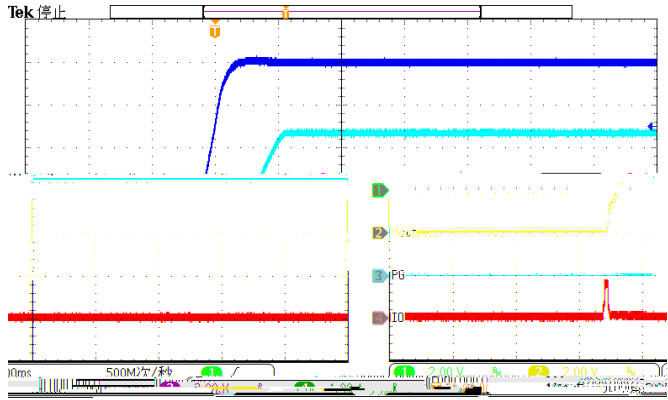
°C

°C

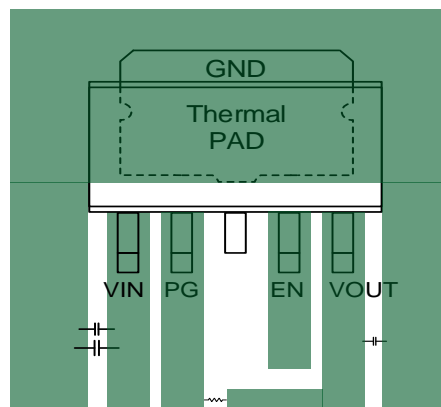
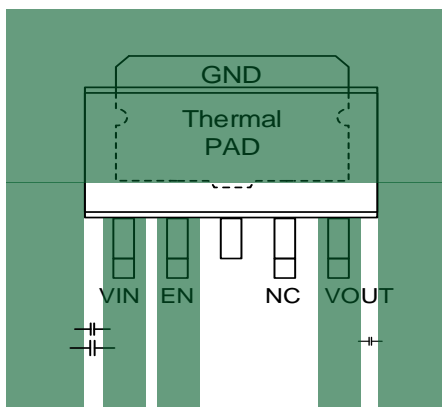
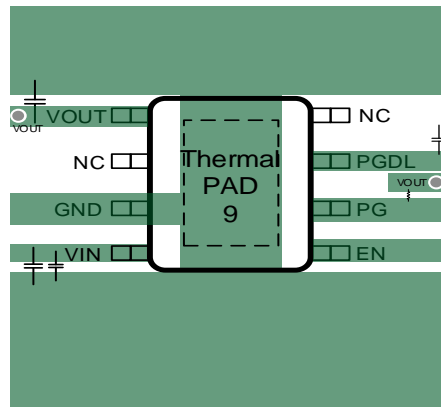
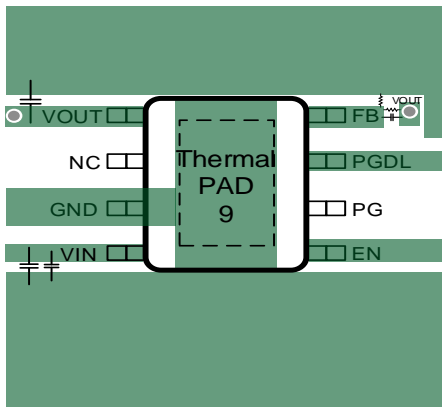
°C

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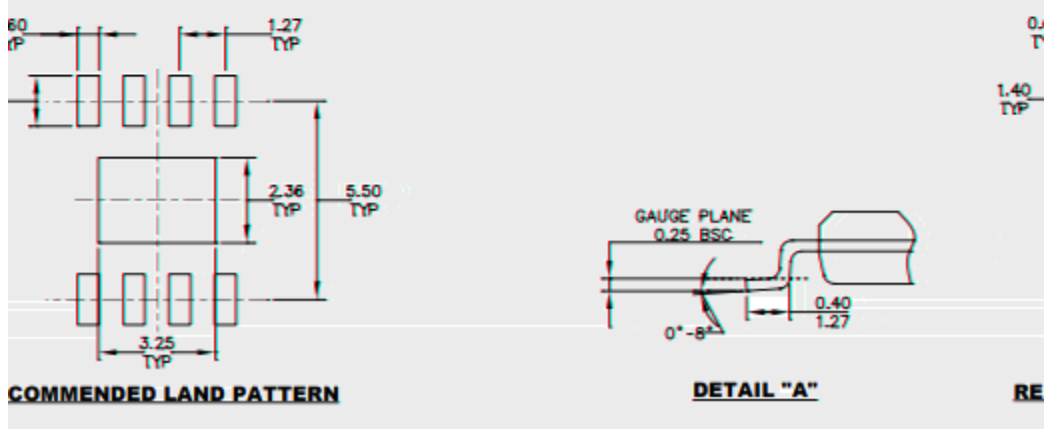
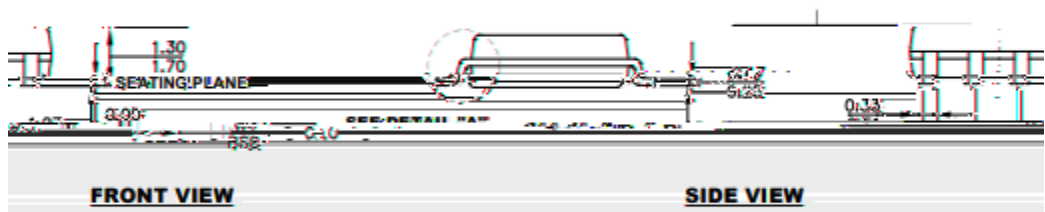
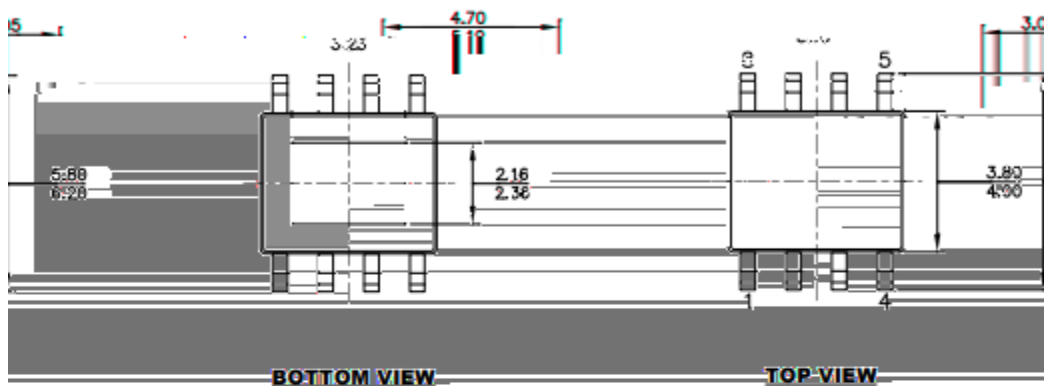


- 1.
- 2.
- 3.
- 4.
- 5.



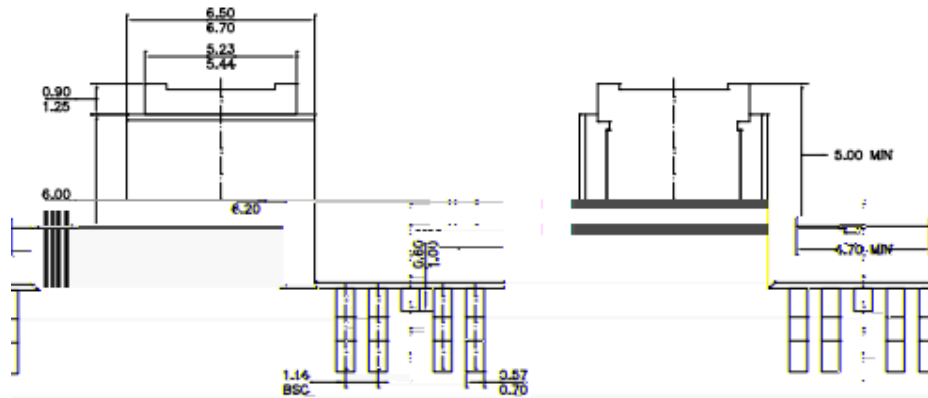
SCIPREP PACKAGE OUTLINE SPECIFICATIONS FOR 0.5mm PITCH 0.25mm BSC

PO 0022 revision 1.0



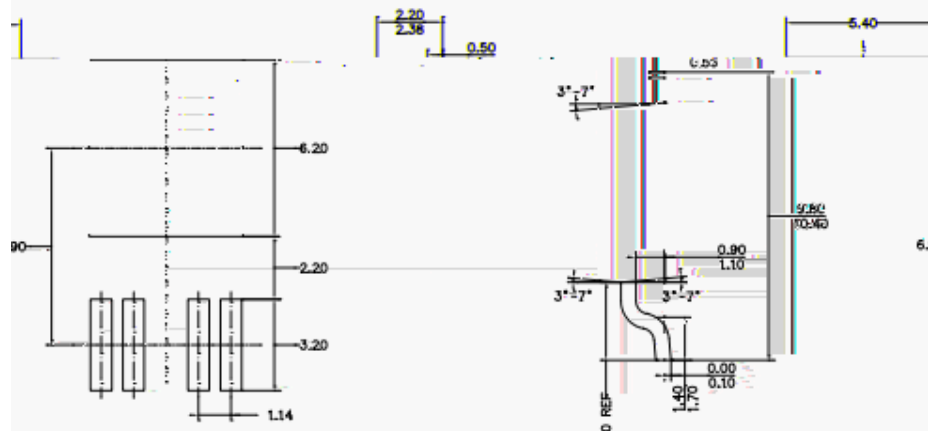
- 1.
- 2.
- 3.
- 4.
- 5.
- 6.

**PACKAGE OUTLINE DRAWING FOR T0252-4L
POD-0040 Revision 0.0**



TOP VIEW

BOTTOM VIEW



RECOMMENDED LAND PATTERN

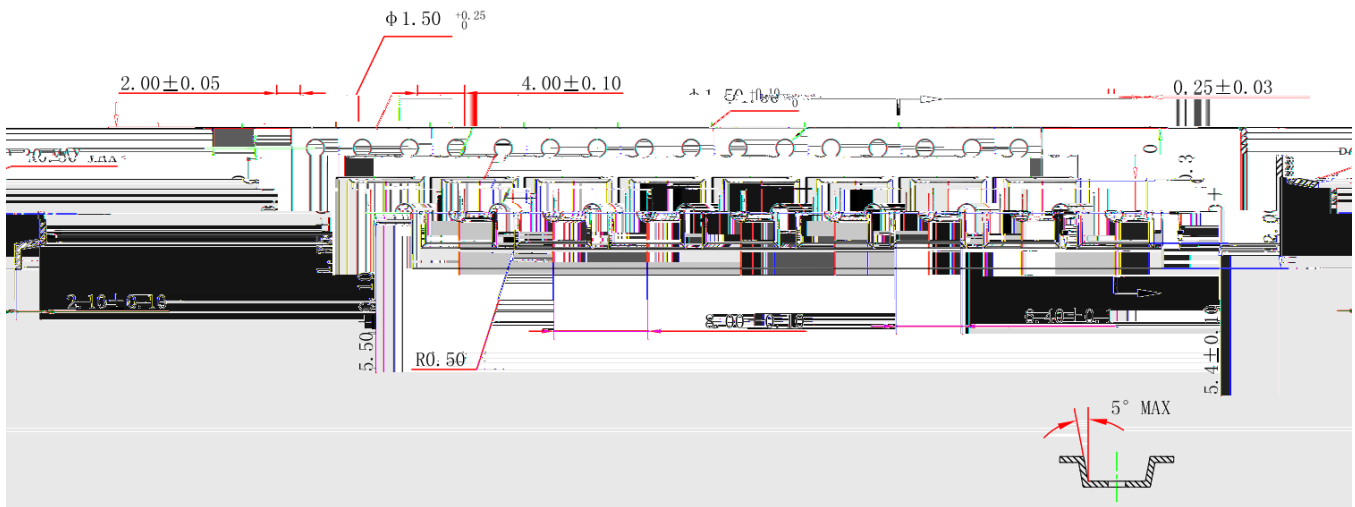
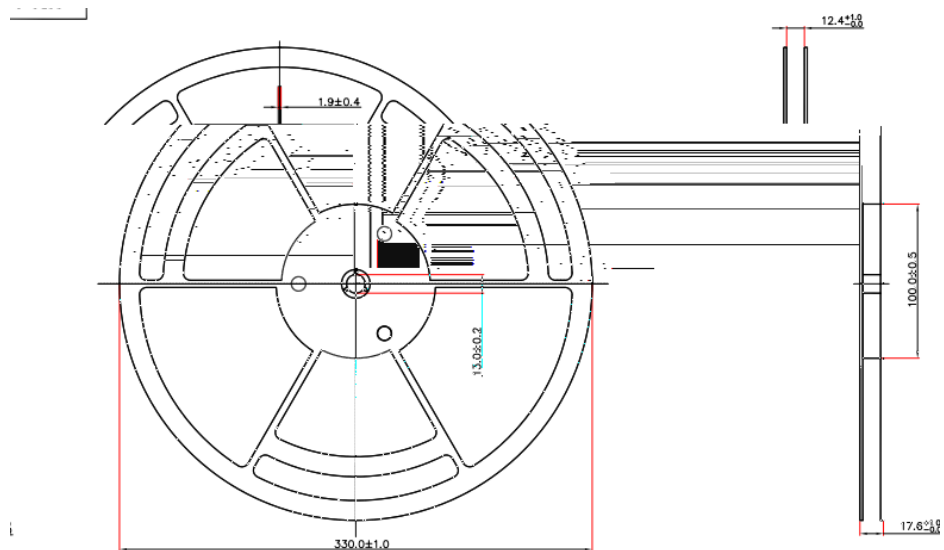
SIDE VIEW

NOTE:

FLASH, PROTRUSIONS OR GATE BURRS.
LEAD FLASH OR PROTRUSIONS.

- 1) ALL DIMENSIONS ARE IN MILLIMETERS.
- 2) PACKAGE LENGTH DOES NOT INCLUDE MOLD
- 3) PACKAGE WIDTH DOES NOT INCLUDE INTERLE
- 4) DRAWING CONFORMS TO JEDEC TO-252.
- 5) DRAWING IS NOT TO SCALE.

7.
8.
9.
10.
11.



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