3V-40V Vin, 500mA, Ultra-Low Quiescent Current LDO with Power-Good

LV

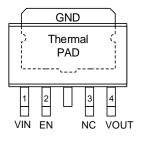


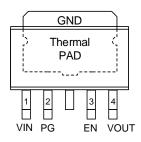
Release to production(). Update the ELECTRICAL CHARACTERISTICS.

	5A00Q	
	F50BQ	
	F33BQ	
	5F50Q	
	5F33Q	
	F50AQ	



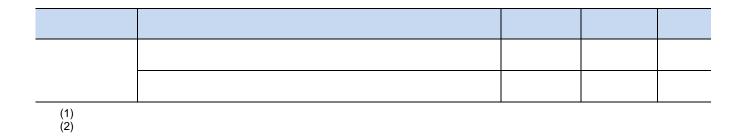














PARAMETER (4-layer)		UNIT
R ja_evm		
PARAMETER (4-layer)		UNIT
R JA_EVM		

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(1)

(2)

(3)

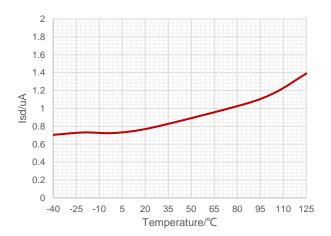








Iload/mA



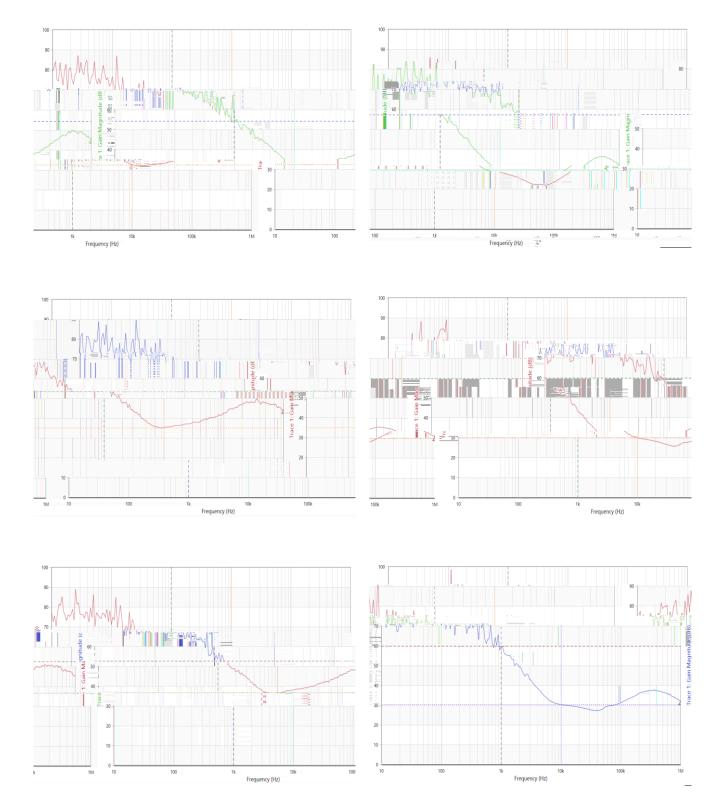


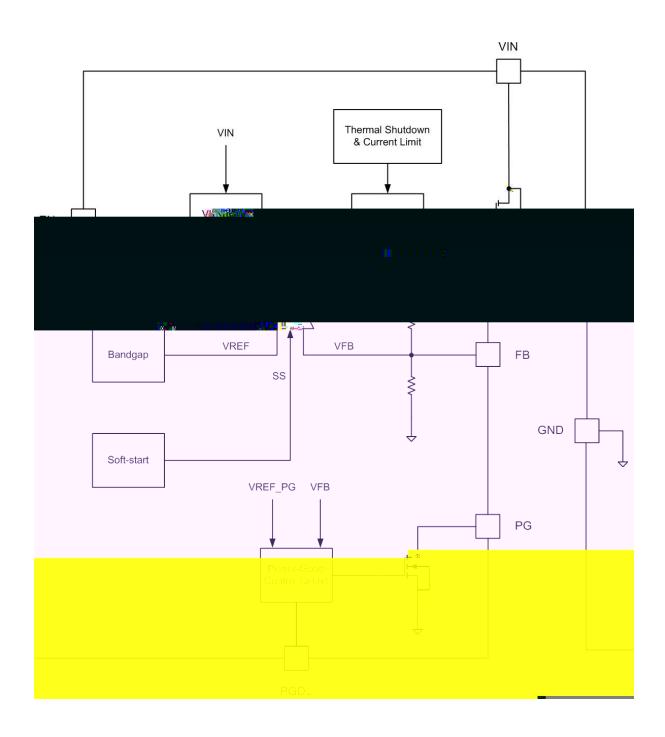
Temperature/°C

Current Limit/mA

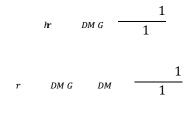
Temperature/°C

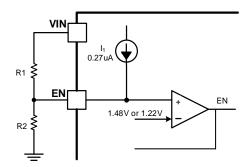






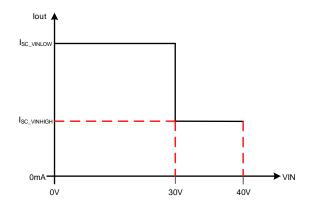


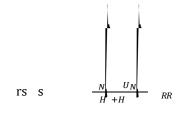




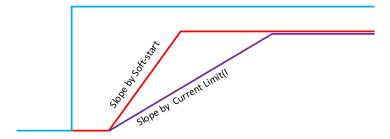


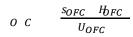
VIN_HIGH Control





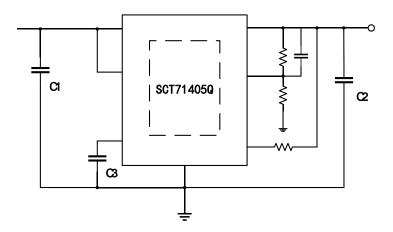


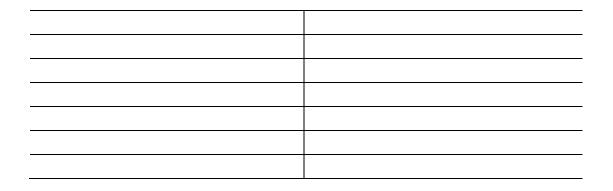


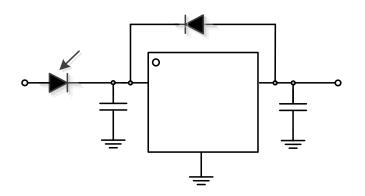


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	COUT/uF																
220uF																	
100uF																	
82uF																	
72uF	Stable																
68uF	but with Poor																
-56aF	Phase Margin						<u>Stehle</u> u	sith Ba	tter-P	hase	Mergi	5 				_	
47uF							C	OUT+	10uF	low E	SR						
33uF																	
22uF																	
15uF		Stable															
10	with Datte		-nais .			2 <u>2</u> .	- 1 99										
					ALT WE												
				1 1818/ 42	31.38ml -												
2.						0.001 0.01	0.05	0.1	0.2	0.3.	0.4	0.5	0.6	0.7	0.8	0.9	



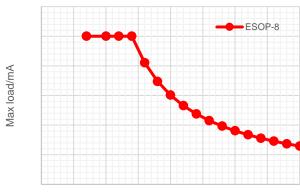
C HM NTS NTS HM MC

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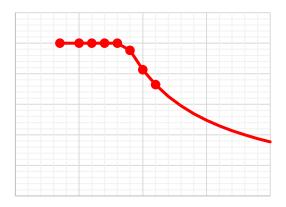
°C	°C
ĩ	°C

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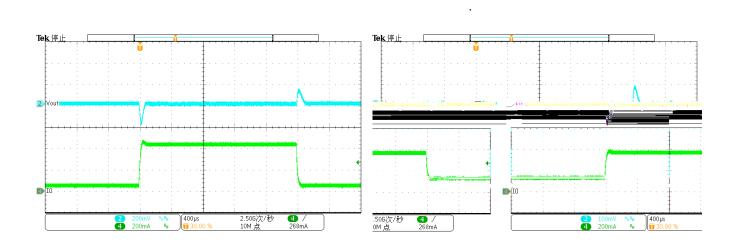
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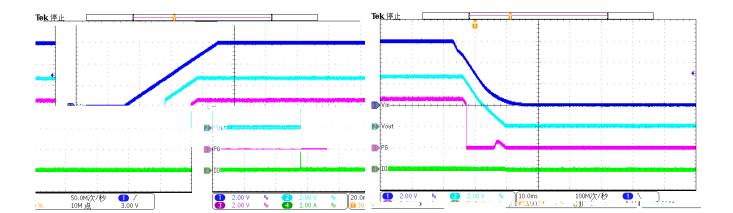
°C °C

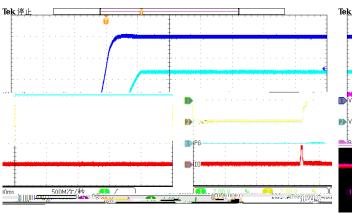


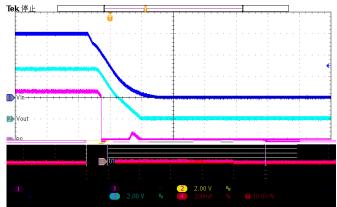






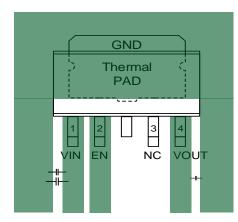


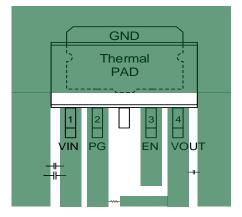


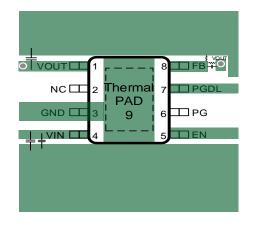


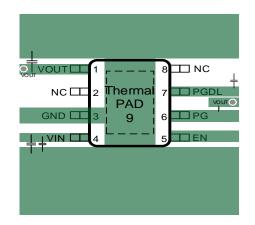




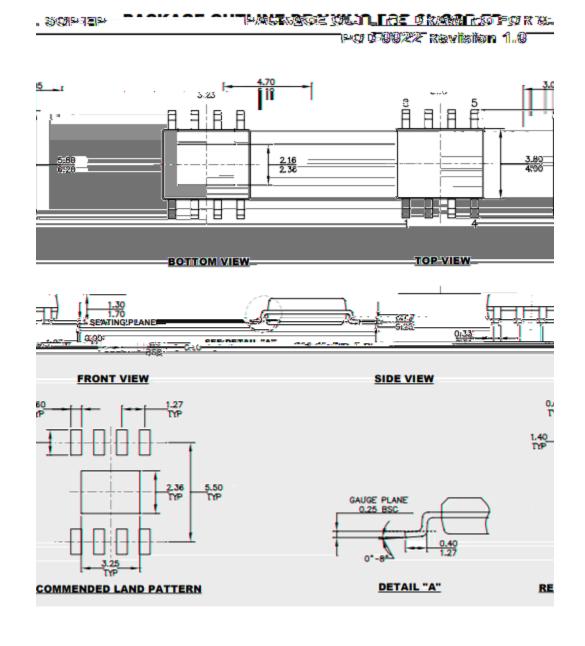






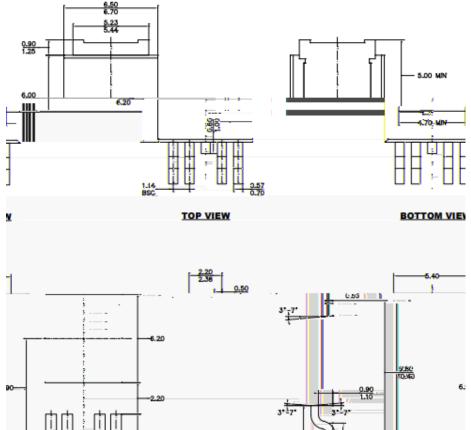


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- 3.
- 4.
- 4.
- 5.



1. 2. 3. 4. 5.





PACKAGE OUTLINE DRAWING FOR TO252-4L POD-0040 Revision 0.0

3.20 1.14 RECOMMENDED LAND PATTERN NOTE:

FLASH, PROTRUSIONS OR GATE BURRS. AD FLASH OR PROTRUSIONS.

1) ALL DIMENSIONS ARE IN MILLIMETERS. 2) PACKAGE LENGTH DOES NOT INCLUDE MOLD 3) PACKAGE WIDTH DOES NOT INCLUDE INTERLE

0.00 98

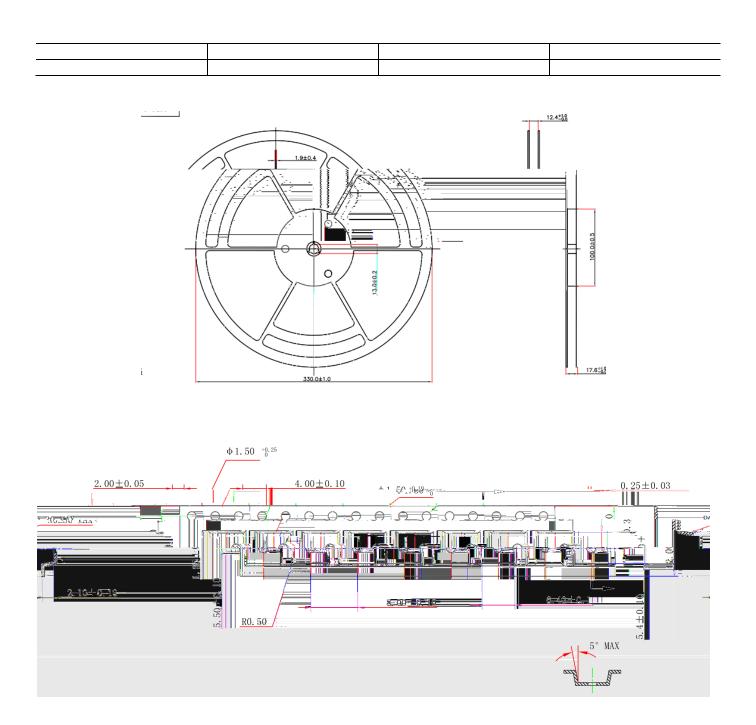
4) DRAWING CONFORMS TO JEDEC TO-252. 5) DRAWING IS NOT TO SCALE.

SIDE VIEW

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2.90





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